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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/801,130	03/16/2004	Georg Eggers	543822005100	9233

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EXAMINER

RAHMAN, FAHMIDA

ART UNIT PAPER NUMBER

2116

DATE MAILED: 09/20/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/801,130

Applicant(s)

EGGERS ET AL.

Examiner

Fahmida Rahman

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 16 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-16 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 16 March 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date 7/27/2004.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_.

### **DETAILED ACTION**

1. Claims 1-16 are pending.

#### **Information Disclosure Statement**

The information disclosure statement (IDS) submitted on 7/27/2004 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is considered by the examiner.

#### **Drawings**

The drawings are objected to because the submitted figures are chopped off in many places. Therefore, numeral 15, 11 in Fig 2 are not pointing to any object.

Line 21 of page 7 of applicant's disclosure mentions that Fig 1 shows terminal 6a. However, 6a is not in Fig 1.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional

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replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

### **Claim Rejections - 35 USC § 102**

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-8, 11, 16 are rejected under 35 U.S.C. 102(b) as being anticipated by Noble et al (US Patent 5914870).

For claim 1, Noble et al teach the following limitations:

A synchronizer signal generator device (202 and 201), which is connected to an electronic system (218 and 222), and which emits a synchronizer signal of a particular frequency (208), which is transferred to at least one device (218 and 222) of the electronic system, wherein the at least one device, of which an impedance (209) is chosen such that a resonance-oscillatory circuit is created (209 forms a resonant circuit), for the synchronizer signal generator device, of

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which the resonance frequency essentially coincides with the frequency of the synchronizer signal (lines 60-65 of column 1).

For claim 2, note lines 47-48 of column 6.

For claim 3, 202 is the driver.

For claim 4, 208 has a rectangular output.

For claim 5, 209 filters 208 to produce sinusoid 246.

For claim 6, 214 is the inductive component.

For claim 7, 212 is the capacitor.

For claim 8, note lines 36-37 of column 6.

For claim 11, the device is a semiconductor device.

For claim 16, Noble et al teach the following limitations:

A process for generating a synchronizer, comprising: emitting a synchronizer signal (208) by a synchronizer signal generator device (202 and 201) to at least one device (218 and 222) of an electronic system; and providing the at least one device in the electronic system, of which an impedance (209) has been selected such that, for the synchronizer signal generator device, a resonance-oscillatory circuit is created (209 forms a resonant circuit), of which the resonance frequency essentially coincides with a frequency of the synchronizer signal (lines 60-65 of column 1).

### **Claim Rejections - 35 USC § 103**

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 9-10, 12-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Noble et al (US Patent 5914870), in view of Cruz et al (US Patent 6396316).

For claim 9, Noble et al do not teach the adjustment of inductive or capacitive component. Cruz et al teach the adjustment of capacitive component (abstract).

It would have been obvious for one ordinary skill in the art at the time the invention was made to combine the teachings of Noble et al and Cruz et al. One ordinary skill in the art would have been motivated to adjust the capacitance to permit the resonant frequency to be adjusted.

For claim 10, neither Noble et al nor Cruz et al teach that the capacitive diode. Examiner takes an official notice that capacitive component is well known in the art. One ordinary skill in the art would be motivated to use the capacitive diode for its advantageous use in voltage controlled oscillator.

For claims 12-15, neither Noble et al nor Cruz et al teach that the synchronization signal is for coordination of data transfer or, an additional signal with lower frequency is generated from the synchronization signal to coordinate data transfer.

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Examiner takes an official notice that using clock to co-ordinate data signal is well known in the art. Using PLL/DLL to produce the secondary clock to latch data is also well known in the art. An ordinary skill in the art would be motivated to use PLL/DLL to produce a secondary clock, which has a lower frequency than primary clock, to latch the data signal since using a secondary clock to latch user entered data signal to the circuit of Noble et al.

### **Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fahmida Rahman whose telephone number is 571-272-8159. The examiner can normally be reached on Monday through Friday 8:30 - 5:30.

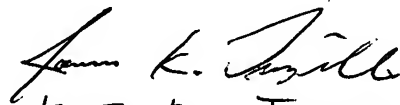
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on 571-272-3670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Fahmida Rahman  
Examiner  
Art Unit 2116

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JAMES K. TRWILLO  
PRIMARY EXAMINER  
TC 2100